Notice of Oral Ex Parte Presentation

June 6th, 2005

Ms. Marlene H. Dortch Secretary Federal Communications Commission 445 12th Street, S.W. Washington, DC 20554

Re: In the Matter of Modification of Parts 2 and 15 of the Commission's Rules for Unlicensed Devices and Equipment Approval, ET Docket No. 03-201

Dear Ms. Dortch:

On May 23rd, 2005, Dr. Ed Liu of nVIDIA Corporation met with Julius Knapp, Hugh VanTuyl, John Reed, Alan Scrime, Karen Rackley, Rashmi Doshi, Joe Dichoso, Steve Dayhoff, Kwok Chan, and Geraldine Matise of the Office of Engineering and Technology regarding the above proceeding.

In the course of this meeting, Dr. Liu (i) clarified the advantages of the partitioned module certification in terms of costs and time-to-market, (ii) discussed how existing modular transmitter approval rules DA 00-1407 unnecessarily forces the BBP/MAC to reside on the RF module, (iii) described ways to ensure noise immunity of data lines between RF and FPGA including the adoption of the robust JC61 electrical signaling and a real-time, echo self-check capability for link error detections, (iv) presented means to ensure specific FPGA implementation matches with various RF modules using a JEDEC industry standard 2-byte manufacturer's code and 2 byte device code, and (v) described a digital method employing an "exclusive-OR" circuit to inject link errors into the digital link for robustness test.

Pursuant to Section 1.1206 of the Commission's Rules, 47 C.F.R. § 1.1206, a copy of this letter is being provided to the abovementioned parties. Please contact the undersigned with any questions in connection with this filing.

Respectfully submitted, /s/ Ed Liu

Ed Liu, Ph. D. Director, Mixed-Signal Design nVIDIA Corporation

cc: Julius Knapp,
Hugh VanTuyl,
John Reed,
Alan Scrime,
Karen Rackley,
Rashmi Doshi,
Joe Dichoso,
Steve Dayhoff,
Kwok Chan
Geraldine Matise